

VERSION WITH MARKINGS TO SHOW CHANGES MADE

In the Specification

The paragraph beginning at page 3, line 3 and ending at page 3, line 9 has been amended as follows:

Log amp 10 will be referred to as part of channel A, which receives the input signal V_A and generates the logarithmic output signal V_{OUT_A} . Likewise, log amp 12 will be referred to as part of channel B, which receives the input signal $V[B]_B$ and generates the logarithmic output signal V_{OUT_B} . For purposes of illustration, the signals utilized in Fig. 4 are shown as single-sided voltages, but the present invention can be realized with differential voltage signals, differential or single-sided current mode signals, or any convenient combination thereof. The logarithmic output signals V_{OUT_A} and V_{OUT_B} are given by the following equations:

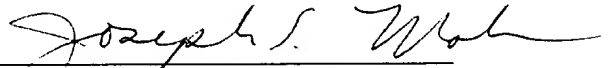
REMARKS

The specification is amended to correct typographical errors.

The Examiner is requested to call the undersigned if any questions arise concerning the above-mentioned application.

Respectfully submitted,

MARGER JOHNSON & McCOLLOM, P.C.

By: 
Joseph S. Makuch
Registration No. 39,286

MARGER JOHNSON & McCOLLOM, P.C.
1030 S.W. Morrison Street
Portland, OR 97205
Telephone: (503) 222-3613